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Complete if Known			
Application Number			
Filing Date		10/20/2003	
First Named Inventor		Madurawwe, Raminda U.	
Group Art Unit			
Examiner Name			
Sheet	1	of	2
Attorney Docket Number			

U.S. PATENT DOCUMENTS

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				Group Art Unit	
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Sheet	2	of	2		

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		SEALS & WHAPSHOT, "Programmable Logic – PLDs and FPGAs", 1997, pp 102-117, McGraw-Hill, USA			<input type="checkbox"/>
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